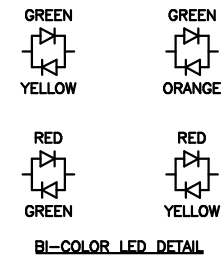


**SUGGESTED PCB LAYOUT**  
(COMPONENT SIDE)



**1.MATERIAL:**

HOUSING: PBT, BLACK, UL94V-0  
 CONTACTS: 0.35[0.014] THICK PHOSPHOR BRONZE PLATED WITH  
 100μ" MINIMUM THICK TIN IN SOLDER AREA.  
 15μ" MINIMUM GOLD IN LOCALIZED PLATE AREA.  
 ENTIRE CONTACTS PLATED WITH 30μ" MINIMUM THICK NICKEL.  
 SHIELDED: 0.25[0.0098] THICK COPPER ZINC ALLOY PLATED WITH  
 30μ" MINIMUM THICK NICKEL.

2.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F.

3.PART NUMBER: HRJM15-S1188-X-T-F1

REFER TO LED OPTIONS TABLE FOR ORDERING CODES

|      |             |                   |             |  |   |
|------|-------------|-------------------|-------------|--|---|
|      |             |                   |             | JOINT TECH ELECTRONIC IND. CO., LTD.<br>JOY DAY INDUSTRIAL CO., LTD. |   |
|      |             | UNIT              | SCALE       | SHEET  | TITLE:  |
|      |             | mm[inch]          |             | 1 of 1   |   |
|      |             | DRAWN BY          | APPROVED BY |  | RJ45, MODULAR JACK,<br>1X1, W/LED, 8P, 8C,<br>SHIELDED, THRU-HOLE |
|      |             | 董正一               | 謝瑞清         |  |   |
|      |             | 2002.02.01        | 2002.02.01  |  |   |
|      |             | GENERAL TOLERANCE |             |  |   |
|      |             | X. X±0.25         | ANGLE±      |  | DRAW NO. :<br>HRJM15-S1188-X-T-F1                                 |
|      |             | X. XX±0.15        |             |  |   |
| REV. | DESCRIPTION | DATE              | X. XXX±0.05 |  | REV. B0   |