



SUGGESTED PCB LAYOUT
(COMPONENT SIDE)

- 1.MATERIAL:
 HOUSING: PBT, BLACK, UL94V-0
 CONTACTS: 0.35[0.014] THICK PHOSPHOR BRONZE PLATED WITH 100 μ m MINIMUM THICK TIN IN SOLDER AREA. 15 μ m MINIMUM GOLD IN LOCALIZED PLATE AREA. ENTIRE CONTACTS PLATED WITH 30 μ m MINIMUM THICK NICKEL.
 SHIELDED: 0.25[0.0098] THICK COPPER ZINC ALLOY PLATED WITH 30 μ m MINIMUM THICK NICKEL.
- 2.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F.
- 3.PART NUMBER: HRJM15-S1188-0-D-F2

				JOINT TECH ELECTRONIC IND. CO., LTD. JOY DAY INDUSTRIAL CO., LTD.	
		UNIT	SCALE	SHEET	TITLE:
		mm[inch]		1 of 1	
		DRAWN BY	APPROVED BY		
		董正一	謝瑞清		
		2002.02.01	2002.02.01		
		GENERAL TOLERANCE			
		X. X±0.25	ANGLE±		
		X. XX±0.15			
		X. XXX±0.05			
BO	NEW RELEASE	2005.12.25			DRAW NO. :
REV.	DESCRIPTION	DATE			HRJM15-S1188-0-D-F2
					REV. B0

RJ45, MODULAR JACK,
1X1, W/O LED, 8P, 8C,
SHIELDED, THRU-HOLE