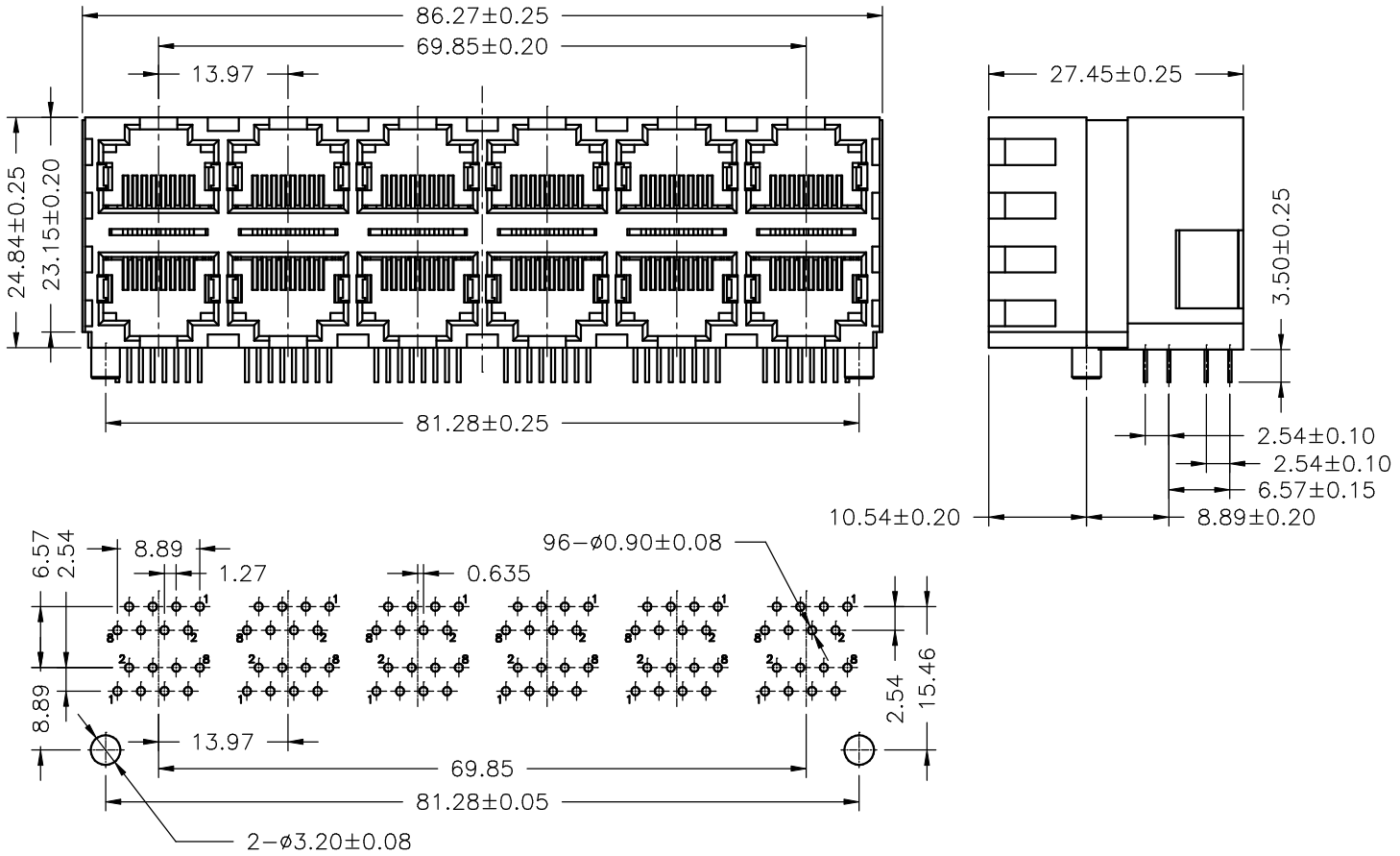

SUGGESTED PCB LAYOUT
(COMPONENT SIDE)

1.MATERIAL:
HOUSING: PBT, BLACK, UL94V-0
CONTACTS: 0.35[0.014] THICK PHOSPHOR BRONZE PLATED WITH
100μ" MINIMUM THICK TIN IN SOLDER AREA.
15μ" MINIMUM GOLD IN LOCALIZED PLATE AREA.
ENTIRE CONTACTS PLATED WITH 30μ" MINIMUM THICK NICKEL.

2.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F.

3.PART NUMBER: HRJ801215-U2688-F1

REV.	DESCRIPTION	DATE
B0	NEW RELEASE	2005.12.25

JOINT TECH ELECTRONIC IND. CO., LTD.
JOY DAY INDUSTRIAL CO., LTD.

UNIT	SCALE	SHEET
mm[inch]		1 of 1

DRAWN BY	APPROVED BY
董正一 2004.10.06	董正一 2004.10.06

GENERAL TOLERANCE

X.X±0.25	ANGLE±
X.XX±0.15	
X.XXX±0.05	

TITLE:
DOUBLE STACK MODULAR JACK,
2X6,W/O LED,8P,8C,
UNSHIELDED,THRU-HOLE

DRAW NO.: HRJ801215-U2688-F1
REV. B0